The Realization and Simulation of the Wireless Ad-hoc Network

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Abstract

The technology of wireless sensor network is a new direction in the field of information science. With the development of science and technology, in modern life, its application is more and more widely. The technology is usually used for information communication in a wide range of network. Sensor network communication can be completed without interference. But the communication process of sensors in some special interferences, such as high-rise block between the sensor or wireless signal interference in the space, communication between wireless sensor network and control terminal may be interrupted. This paper proposes an efficient, stable self-organizing network protocol algorithm in the situation, to realize self organization and information communication. Using STM32 microcontroller as network nodes, it achieves a low power, high speed, high performance wireless ad hoc network, the node and terminal can form a stable network communication, quality of the network communication can be further improved. And compared with the AODV by using NS2, verified the feasibility of routing protocol in this paper.

Keywords: Wireless network; self organization; microcontroller; sensor; NS2

1. Introduction

The wireless network is composed of a group of mobile nodes with wireless transceiver components, it is a self-organizing and multi hop network. Compared with other communication networks, wireless ad hoc network has no center, self-organizing, multi hop routing, dynamic topology and other characteristics. It has no cable fixed base and each node can be used as a terminal and routing which can switch fast and movie at any time.

It is necessary to keep the communication with other nodes for the nodes in wireless network, so the design of nodes should have a low power consumption and strong communicating ability. STM32 microcontroller is based on ARM Cortex-M3. It is designed for embedded applications wich require high performance, low cost, low power. Using the ARM chip as the node design, nodes can be suitable for various external facilities, and have strong develop ability. It will be more extensive used. This paper mainly describes the use of ARM MCU process from the set of functions of wireless network.

2. Design Principle

This design uses STM32F103 MCU and does not need the complex hardware circuit. It will send and receive data by the serial port 2 which is connected with the wireless module. The design realizes the network between the multi nodes and terminal by using a self-designed algorithm. It includes a terminal and a plurality of nodes in the system. The terminal is always open. It will receive and deal with the data sending from nodes, then responds to the nodes. It can also sending commands by the serial port 1 connected with the PC. After tuning on the power, the node will connect with the terminal automatically. If the connection is failed, the node will try to connect with other nodes which are in the network. If successes, it

ISSN: 2233-7857 IJFGCN Copyright © 2014 SERSC will transfer data and help the nodes in the blind spots connecting with the network. Figure 2-1 is the block diagram of the terminal. Figure 2-1 is the block diagram of the nodes.

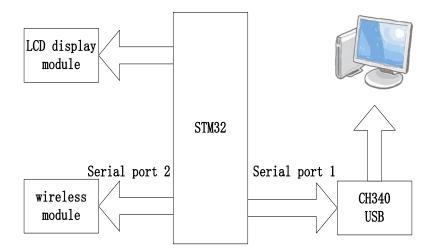


Figure 2-1. The Block Diagram of The Terminal

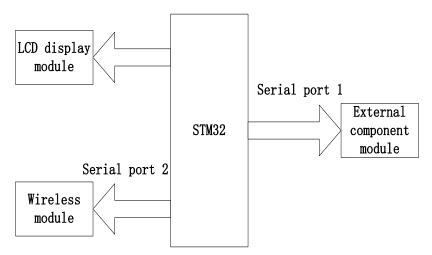


Figure 2-2. The Block Diagram of the Nodes

This algorithm can ensure the communication between nodes and terminal can connect through other routes network when the signal interference happened, and search the blind spot where the nodes cannot reach. Figure 2-3 is the system network structure diagram.

In the diagram, the node A and node B can communicate with a terminal network, and node C is out of the transmission range in the terminal signals but in the transmission range of the node A and B. So the node C can connect with the network through the node A and B.

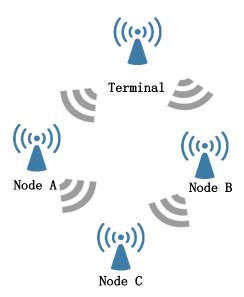


Figure 2-3. Diagram of the System Network Structure Diagram

3. Self-organization Protocol

The main challenge of Ad Hoc is that how the distributed routing database saved in the node adapt to the dynamic change of network topology. At present, the typical routing protocol includes AODV, OLSR and DSR, *etc*.

In this paper, the self-organization routing protocol is improved base on AODV. One is after receiving the routing request, it will treat the node load as the measurement of the responding time. If the node load is smaller, the delay time is shorter. Second is that each node saves the node address in the same link. It reduces the time of transmitting data's and guarantees the success rate of transmission.

3.1. The Protocol Format

Self-organization protocol format as follows:

START ID Aim Local	Command	Check	END	
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Among them, START is data start mark bit; ID represents the identity, used to distinguish different case data, nodes will make corresponding processing operation according to different identity; Aim indicates that the target address data; Local is the source address, nodes send data content itself; Command represents the data command, the single-chip microcomputer according to the corresponding command execution program; Check is inspection mark, can judge whether the node in the network, to avoid making mistake operation; END indicates the end of data flag.

3.2. Network Connect Process

The network includes a terminal connecting with PC and multiple nodes. After tuning on power of terminal and the initialization, enter the mode waiting for the command. Nodes can be arbitrarily placed, after power on and initialization, nodes firstly send a request command of connecting with the network to the terminal, if the terminal confirmation command is received, it have been accessed to and do the operation in the network. If in the set time, it

can't receive terminal confirm command, that node thinks itself as the blind spot, the node will send the request to the other network node, if the received the node confirm command, have been in the network and saved the address of node. Other node network courses are the same, each node can mount a number of node, the networking can send commands to every node. Self-organization process is shown in Figure 3-1.

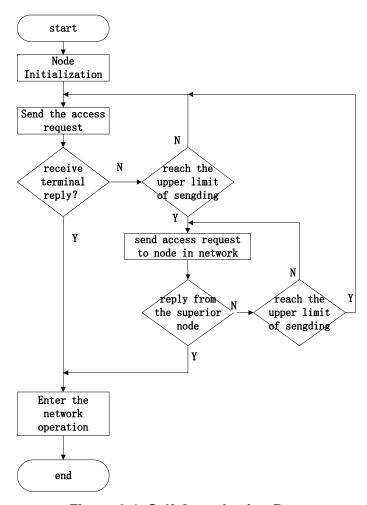


Figure 3-1. Self-Organization Process

4. The Experimental Simulation and Analysis

In this paper, it makes the network simulation by using the NS2, and compares the simulation result with AODV. It concludes the performance of the protocol. The experimental simulation results are shown in Figure 4-1, 4-2, 4-3. This paper chooses three common indexes which are used in routing protocol performance: Packet Delivery Ratio, Average Delay, and Routing Load, to evaluate the network performance.

4.1. Establishment of Simulation Environment

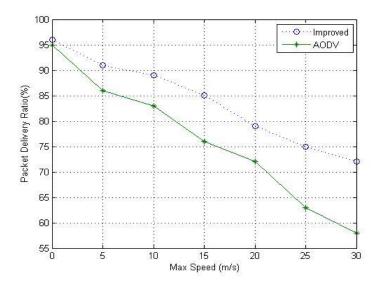


Figure 4-1. Packet Delivery Ratio

According to the result of experiment, when the node's max speed is low, the packet delivery ratio in this paper is higher than AODV. Because when choosing route, it will select the node which has lighter load, so that reducing the probability of discarded packets. With the increase of the max speed, the node in the network may be disconnected, so the node in the disconnected has to save the data, thus it increases the probability that the packet will be lost when the node's cache queue is heavy, causing the packet delivery ratio drop. When the max speed is high, the packet delivery ratio of the routing protocol in this paper is higher than AODV, and drop slowly. Because it takes into consideration the node load when finding the routing, thus it greatly reduces the probability of discarding the data.

4.2. Average Delay

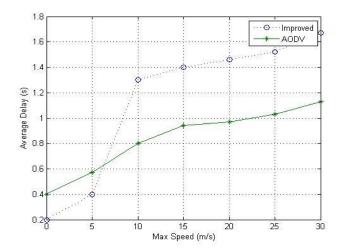


Figure 4-2. Average Delay

According to the Figure 4-2, we can see when node movement speed is low, in this paper, the routing protocol routing delay than AODV routing protocol. But with the increase of the max speed, the average delay is lower than AODV. Because when sending the routing request, it cannot directly use the node routing cache, only the destination node can return RREP, so increasing the routing request time. When the max speed gets higher, the probability of disconnected increases, the use of AODV cache reduced. The node selection of protocol in this paper is relatively stable, and it reduces the probability of the link broken, so the protocol's average delay is lower than AODV.

4.3. Establishment of Simulation Environment

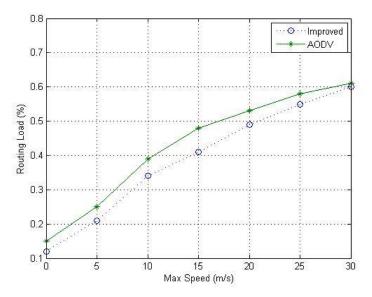


Figure 4-3. Routing Load

According to the Figure 4-3, we can see the routing load of the protocol in this paper is smaller than AODV. For two reasons: one is that the performance of the selected node in this paper is higher, so the routing path repair time caused by the node is small, so reduce the cost of the routing repair. Secondly, when it does the routing repair after the disconnection of the routing link,the cost is less than AODV. Because the AODV scope is bigger when the routing repair, and it will send the RERR to the source nodes which are all through it.

5. Conclusions

This paper presents a protocol for wireless ad hoc networks algorithm based on single chip microcomputer. In the self-organization protocol, it will do the logic judgment through the program to receiving data, realize the communication between nodes. The algorithm can optimize transmission route of the network data, and can ensure the correctness of data. The data transmission network is more stable, more efficient. Through practical experiments, this MCU use of wireless ad hoc network protocol can achieve excellent network performance in the case of signal jamming. And compared with the AODV by using NS2, verified the feasibility of routing protocol in this paper.

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